



LAP 74

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. No.	10/618,316 / 4779
				Filing Date	July 11, 2003
				First Named Inventor	Patrick Lysaght
				Art Unit	2825
				Examiner Name	Vuthe Siek
Sheet	2	of	2	Attorney Docket Number	X-1410 US

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Monica Alderighi et al.; "A Fault-Tolerant FPGA-Based Multi-Stage Interconnection Network for Space Applications"; published Computer Society; pp. 1-5.	
		Ian Kyles; "Creating Large Switch Fabrics using the Three-Stage (Clos) Architecture"; Copyright VITESSE Semiconductor Corporation; pp. 1-12.	
		Steve Young et al.; "A High I/O Reconfigurable Crossbar Switch"; pp. 1-8.	

Examiner Signature		Date Considered	
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